

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	20	sidiropoulos.in.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/13 10:04
S2	3463	375/354,"357","371","373","374", "376","377".ccls.	USPAT	OR	OFF	2002/04/11 15:28
S3	388	375/354,"357","371","373","374", "376","377".ccls. and delay adj line	USPAT	OR	OFF	2002/04/11 15:35
S4	1598	713/400,"401","500","503","600", "601".ccls.	USPAT	OR	OFF	2002/04/11 15:43
S5	147	713/400,"401","500","503","600", "601".ccls. and delay adj line	USPAT	OR	OFF	2002/04/11 15:35
S6	49	327/147-150, 156-159.ccls.	USPAT	OR	OFF	2002/04/11 15:47
S7	1235	327/239,"269","270","271","272", "274","276","277","280","284".ccls.	USPAT	OR	OFF	2002/04/11 15:51
S8	384	327/239,"269","270","271","272", "274","276","277","280","284".ccls. and delay adj line	USPAT	OR	OFF	2002/04/11 16:06
S9	3979	327/530,"534","535","536","537", "538","540".ccls.	USPAT	OR	OFF	2002/04/11 16:09
S10	27	327/530,"534","535","536","537", "538","540".ccls. and delay adj line	USPAT	OR	OFF	2002/04/11 16:16
S11	561	455/260.ccls.	USPAT	OR	OFF	2002/04/11 16:22
S12	11	455/260.ccls. and delay adj line	USPAT	OR	OFF	2002/04/11 16:44
S13	1072	315/291.ccls.	USPAT	OR	OFF	2002/04/11 16:51
S14	2	315/291.ccls. and delay adj line	USPAT	OR	OFF	2002/04/11 16:52
S15	12	delay adj line and comparator and charge-pump and amplifier	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 16:53
S16	85	delay adj line and comparator and charge adj pump and amplifier	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 16:55
S17	85	(delay adj line and comparator and charge adj pump and amplifier) and voltage	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 16:54
S18	73	delay adj line and charge adj pump and amplifier and voltage and (phase adj comparator or phase adj detector)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 18:49
S19	1	"6247138".pn. and capacitor	USPAT	OR	OFF	2002/04/11 18:45
S20	1	"6247138".pn. and amplifier	USPAT	OR	OFF	2002/04/11 18:46

S21	1	"6247138".pn. and differential	USPAT	OR	OFF	2002/04/11 18:47
S22	902	delay adj line with differential	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 18:48
S23	158	cmos adj inverter with differential	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 18:49
S24	5	delay adj line and charge adj pump and amplifier and (voltage adj regulat\$3) and (phase adj comparator or phase adj detector)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 18:57
S25	7	("4922141" "5473277" "5532653" "5576656" "5596297" "6011428" "6046624").PN.	USPAT	OR	OFF	2002/04/11 18:50
S26	22941	(operational or differential) adj amplifier with capacitor	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 19:01
S27	94	((operational or differential) adj amplifier with capacitor) with (voltage adj regulat\$3)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 19:02
S28	113	voltage with (delay adj line) same regulat\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 19:03
S29	99	cmos adj inverter same regulat\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/11 19:03
S30	373	((operational or differential) adj amplifier or op adj amp) with capacitor with voltage adj follower	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:28
S31	47	power adj supply adj noise with (filter or filtering) with capacitor	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 09:33
S32	3092	((operational or differential) adj amplifier or op adj amp) with capacitor with (filter or filtering)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:03
S33	192	((operational or differential) adj amplifier or op adj amp) with capacitor with (bypass or bypassing)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 09:47
S34	0	"6247138".pn. and multiplexer	USPAT	OR	OFF	2002/04/12 09:47
S35	1	"6247138".pn. and fine	USPAT	OR	OFF	2002/04/12 09:49

S36	0	(core adj loop) and multiplexer and (interpolator or interpolating or interpolation)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 09:51
S37	5	clock adj alignment and multiplexer and (interpolator or interpolating or interpolation)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 09:52
S38	373	delay adj line and multiplexer and (interpolator or interpolating or interpolation)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 09:53
S39	164	((operational or differential) adj amplifier or op adj amp) with capacitor with (filter or filtering)) with noise	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:11
S40	326	differential same inverter same noise	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:12
S41	3	(differential same inverter same noise) same delay adj line	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:12
S42	46	(differential same inverter same noise) same delay	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:17
S43	10069	pmos with nmos	USPAT	OR	OFF	2002/04/12 10:21
S44	128	((operational or differential) adj amplifier or op adj amp) with capacitor with bias adj current	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:29
S45	210	((operational or differential) adj amplifier or op adj amp) with bias adj current with control	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:33
S46	1	((operational or differential) adj amplifier or op adj amp) with bias adj current with bias adj signal	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 11:45
S47	80	charge adj pump same current adj source same transistor same capacitor	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2002/04/12 10:40
S48	1804	331/1a,"8","34".ccls.	USPAT	OR	OFF	2002/04/12 11:47
S49	1788	365/233.ccls.	USPAT	OR	OFF	2002/04/12 11:52
S50	224	(331/1a,"8","34".ccls. or 365/233.ccls.) and delay adj line	USPAT	OR	OFF	2002/04/12 11:53

S51	34	((331/1a,"8","34".ccls. or 365/233. ccls.) and delay adj line) and charge adj pump	USPAT	OR	OFF	2002/04/12 11:53
S52	2112	delay same source with control with voltage	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 16:47
S53	9	S52 same delay adj locked adj loop	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/07 17:04
S54	30	delay adj element same variable adj delay same source with control\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 11:03
S55	1	"6316987".pn.	USPAT	OR	OFF	2004/10/08 11:13
S56	7	("4922141" "5473277" "5532653" "5576656" "5596297" "6011428" "6046624").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/10/08 11:20
S57	7	static adj cmos adj inverter\$1 same delay	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 12:08
S58	0	static adj cmos same variable adj delay	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 15:12
S59	107	static adj cmos same delay	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 13:45
S60	26	static adj cmos with advantage	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 12:11
S61	21	static adj cmos same delay ADJ ELEMENT\$1	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 13:45
S62	4	("4620180" "4929850" "5012141" "5122694").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/10/08 13:50
S63	16	("5229668").URPN.	USPAT	OR	OFF	2004/10/08 13:53
S64	1	static adj cmos same delay same voltage with source	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 15:13

S65	41	static adj cmos same delay same voltage	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/08 15:13
S66	139999	electrode with gate	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/12 14:32
S67	69	supply adj electrode with gate	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/12 14:35
S68	3094	supply adj electrode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/12 15:02
S69	75680	base with electrode	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/12 15:10
S70	69	S68 with gate	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/12 15:02
S71	4646	gate adj electrode with supply	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/12 15:10